

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cho, *et al.* Docket No.: TI-36298
Filed: Herewith Examiner: TBD
Serial No.: TBD Art Unit: TBD
For: Built-In Self Test Method and Apparatus for Jitter Transfer and Jitter Tolerance

Mail Stop: Patent Application
Commissioner for Patents
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Alexandria, VA 22313-1450

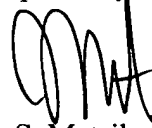
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Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Respectfully submitted,



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February 25, 2004

Date

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				Application Number	TBD
				Filing Date	Herewith
				First Named Inventor	Cho, <i>et al.</i>
				Art Unit	TBD
				Examiner Name	TBD
				Attorney Docket Number	TI-36298
Sheet	1	of	1		

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Examiner Signature		Date Considered	
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